

Notice of Allowability	Application No.	Applicant(s)	
	09/053,006	ABDALLAH ET AL.	
	Examiner	Art Unit	
	Kimbinh T. Nguyen	2628	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to amendment filed 04/19/06.
2. ☒ The allowed claim(s) is/are 1,2,4-9 and 11-23.
3. ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 - a) ☐ All b) ☐ Some* c) ☐ None of the:
 1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

* Certified copies not received: _____.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.

THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

4. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
 5. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
 - (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
 - 1) ☐ hereto or 2) ☐ to Paper No./Mail Date _____.
 - (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.
- Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
6. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

- | | |
|---|--|
| 1. <input type="checkbox"/> Notice of References Cited (PTO-892) | 5. <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| 2. <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 6. <input type="checkbox"/> Interview Summary (PTO-413),
Paper No./Mail Date _____. |
| 3. <input type="checkbox"/> Information Disclosure Statements (PTO-1449 or PTO/SB/08),
Paper No./Mail Date _____ | 7. <input type="checkbox"/> Examiner's Amendment/Comment |
| 4. <input type="checkbox"/> Examiner's Comment Regarding Requirement for Deposit
of Biological Material | 8. <input checked="" type="checkbox"/> Examiner's Statement of Reasons for Allowance |
| | 9. <input type="checkbox"/> Other _____. |

DETAILED ACTION

Terminal Disclaimer

1. The terminal disclaimer filed on 04/19/06 disclaiming the terminal portion of any patent granted on this application which would extend beyond the expiration date of Patent No. 5,835,748 has been reviewed and is accepted. The terminal disclaimer has been recorded.

Allowable Subject Matter

2. Claims 1, 2, 4-9, 11-23 are allowed.

The following is an examiner's statement of reasons for allowance:

Claims 1 and 8, the prior art does not teach a hardware unit to transmit data representing graphics to another computer; a processor coupled to the hardware unit; and a storage device coupled to the processor and having stored therein a routine, which when executing by the processor, causes the processor to generate the data, the routine at least causing the processor to access a first data operand having a data element; access a second packed data operand having at least two data elements; insert the data element in the first data operand into a destination field of a destination register, wherein the storage device further comprises a packing device for packing integer data into the data elements.

Claim 4, the prior art does not teach a storage device coupled to the processor and having stored therein a routine, which when executing by the processor, causes the processor to generate the data, the routine at least causing the processor to access a first packed data operand having at least two data elements; and extract one of the data

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elements from the first packed data operand into a field of a destination register, while preserving other data elements in the first packed data operand.

Claim 11, the prior art does not teach decoding a single instruction; in response to decoding the single instruction, accessing a first packed data operand having at least two data elements; and extracting one of the data elements from the first packed data operand into a field of a destination register, while reserving other data elements in the first packed data operand.

Claim 15, the prior art does not teach altering the data using three-dimensional geometry to generate a second three-dimensional image, altering at least including, accessing a first data operand having a data element; accessing a second packed data operand having at least two data elements; inserting the data element in the first data operand into a destination field of a destination register; and displaying the second three-dimensional image.

Claim 19, the prior art does not teach accessing data representative of a first three-dimensional image; altering the data using three-dimensional geometry to generate a second three-dimensional image including accessing a first packed data operand having at least two data elements; and extracting one of the data elements from the first packed data operand into a field of a destination register; and displaying the second three-dimensional image.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably

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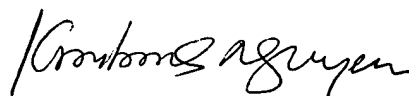
accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

3. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Kimbinh T. Nguyen whose telephone number is (571) 272-7644. The examiner can normally be reached on Monday to Thursday from 7:00 AM to 4:30 PM. The examiner can also be reached on alternate Friday from 7:00 AM to 3:30 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Michael Razavi can be reached at (571) 272-7664. The fax phone number for the organization where this application or proceeding is assigned is (571) 273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

June 23, 2006



**KIMBINH T. NGUYEN
PRIMARY EXAMINER**